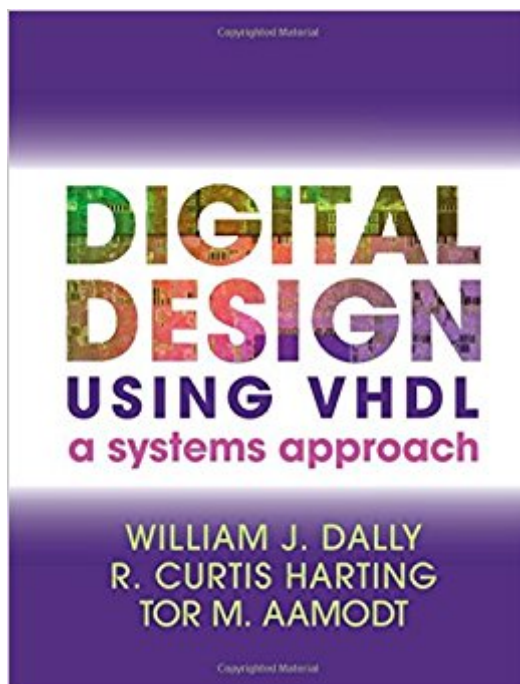The book was found

Digital Design Using VHDL: A Systems Approach

Synopsis

This introductory textbook provides students with a system-level perspective and the tools they need to understand, analyze and design digital systems. Going beyond the design of simple combinational and sequential modules, it shows how such modules are used to build complete systems, reflecting real-world digital design. All the essential topics are covered, including design and analysis of combinational and sequential modules, as well as system timing and synchronization. It also teaches how to write VHDL-2008 HDL in a productive and maintainable style that enables CAD tools to do much of the tedious work. A complete introduction to digital design is given through clear explanations, extensive examples and online VHDL files. The teaching package is completed with lecture slides, labs and a solutions manual for instructors. Assuming no previous digital knowledge, this textbook is ideal for undergraduate digital design courses that will prepare students for modern digital practice.

Book Information

Hardcover: 721 pages

Publisher: Cambridge University Press; 1 edition (March 9, 2016)

Language: English

ISBN-10: 1107098866

ISBN-13: 978-1107098862

Product Dimensions: 7.4 x 1.4 x 9.7 inches

Shipping Weight: 1.6 pounds (View shipping rates and policies)

Average Customer Review: Be the first to review this item

Best Sellers Rank: #884,017 in Books (See Top 100 in Books)   #108 inÃ Â Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Integrated   #269 inÃ Â Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Design   #301 inÃ Â Books > Engineering & Transportation > Engineering > Electrical & Electronics > Digital Design

Customer Reviews

Provides students with a system-level perspective and the tools they need to understand, analyze and design complete digital systems using VHDL. It goes beyond the design of simple combinational and sequential modules to show how such modules are used to build complete systems, reflecting digital design in the real world.

William J. Dally is the Willard R. and Inez Kerr Bell Professor of Engineering at Stanford University and Chief Scientist at NVIDIA Corporation. He and his group have developed system architecture, network architecture, signaling, routing and synchronization technology that can be found in most large parallel computers today. He is a Member of the National Academy of Engineering, a Fellow of the IEEE, a Fellow of the ACM and a Fellow of the American Academy of Arts and Sciences. He has received numerous honors including the ACM Eckert-Mauchly Award, the IEEE Seymour Cray Award and the ACM Maurice Wilkes Award.R. Curtis Harting is a Software Engineer at Google and holds a PhD from Stanford University. He graduated with honors in 2007 from Duke University with a BSE, majoring in Electrical and Computer Engineering and Computer Science. He received his MS in 2009 from Stanford University.Tor M. Aamodt is an Associate Professor in the Department of Electrical and Computer Engineering at the University of British Columbia. Alongside his graduate students, he developed the GPGPU-Sim simulator. Three of his papers related to the architecture of general purpose GPUs have been selected as 'Top Picks' by IEEE Micro Magazine and one as a 'Research Highlight' by Communications of the ACM magazine. He was a Visiting Associate Professor in the Computer Science Department at Stanford University during his 2012-2013 sabbatical, and from 2004 to 2006 he worked at NVIDIA on the memory system architecture ('framebuffer') of the GeForce 8 Series GPU.

Digital Design Using VHDL: A Systems Approach Digital Systems Design Using VHDL Advanced Digital Logic Design Using VHDL, State Machines, and Synthesis for FPGA's Digital Design with RTL Design, VHDL, and Verilog Digital Electronics: A Practical Approach with VHDL (9th Edition) Digital Logic and Microprocessor Design with VHDL Fundamentals of Digital Logic with VHDL Design Digital Design with CPLD Applications and VHDL RTL Hardware Design Using VHDL: Coding for Efficiency, Portability, and Scalability Design Recipes for FPGAs, Second Edition: Using Verilog and VHDL Digital Design (Verilog): An Embedded Systems Approach Using Verilog Digital Fundamentals with VHDL Introduction to Embedded Systems: Using ANSI C and the Arduino Development Environment (Synthesis Lectures on Digital Circuits and Systems) Signals and Systems using MATLAB, Second Edition (Signals and Systems Using MATLAB w/ Online Testing) Career Building Through Using Digital Design Tools (Digital Career Building) The Adobe Photoshop Lightroom: 17 Tips You Should Know to Get Started Using Photoshop Lightroom (For Digital Photographers) (Graphic Design, Adobe Photoshop, Digital Photography, Lightroom) Circuit Design and Simulation with VHDL (MIT Press) Introduction to Logic Circuits & Logic Design with VHDL Circuit Design with VHDL Digital Systems Design and Prototyping: Using Field Programmable Logic

and Hardware Description Languages

Contact Us

DMCA

Privacy

FAQ & Help